

**Response Under 37 CFR § 1.116  
Expedited Procedure  
Art Unit 2116**

1374.43352X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): H. NAMBU, et al  
Serial No.: 10/768,441  
Filed: January 30, 2004  
For: SEMICONDUCTOR INTEGRATED CIRCUIT  
ART UNIT: 2116  
EXAMINER J. SUGENT

**AMENDMENT AFTER FINAL REJECTION**

**MS: Amendments AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

June 8, 2007

Sir:

In response to the Final Office Action dated February 8, 2007, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendment of the Claims; and**

**Remarks are included following the amendments.**